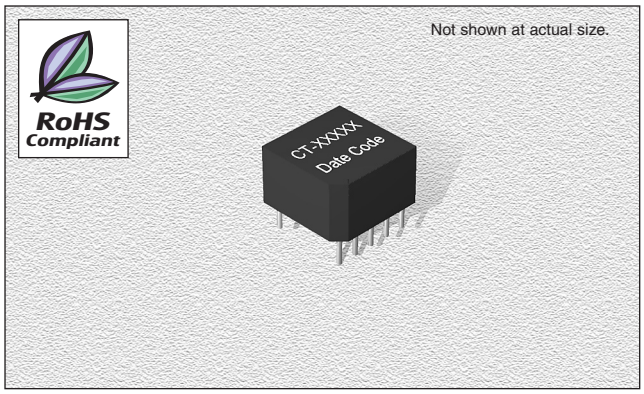
T1-CEPT-ISDN-PRI

10 Pin Dip



APPLICATIONS

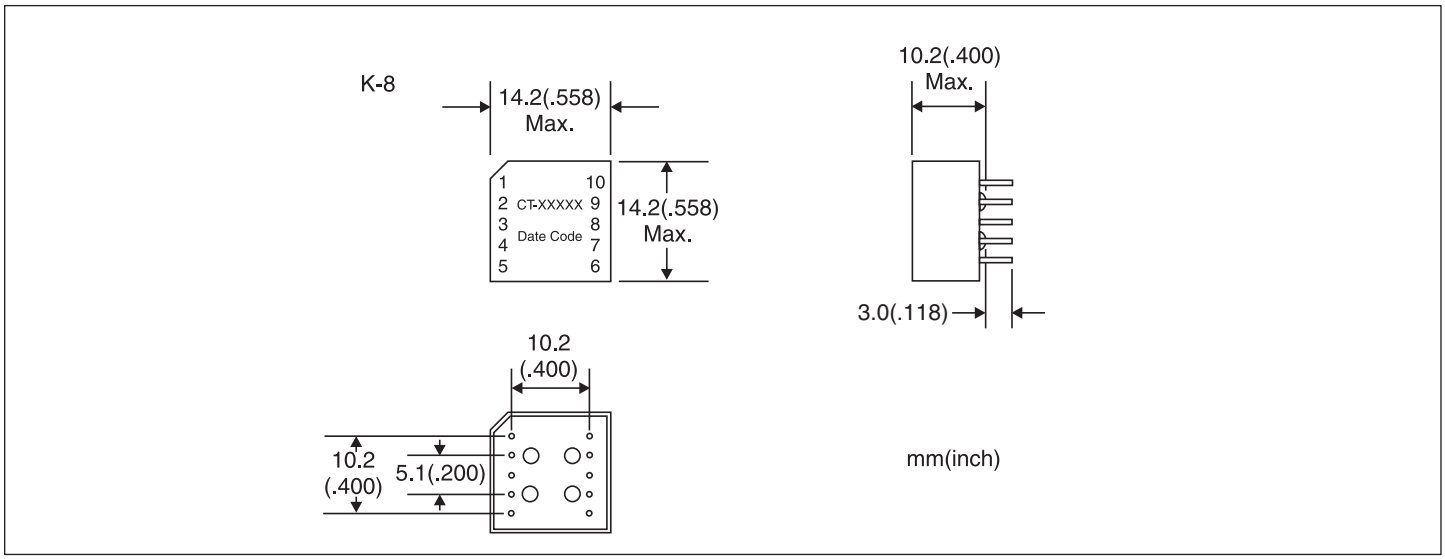
- Certified for heavy insulation per EN 41003/EN 60950
- For T1/CEPT line interfaces
- 3K Vrms isolation
- Matched to leading transceiver ICs
- Designed to meet ITU-T G.703
- Operating Temperature: 0°C to +70°C
- Storage Temperature: -25°C to +125°C
- RoHS Compliant

Electrical Specifications @ 25°C

ORDERING INFO

Part Number	Turns Ratio (pri:sec ± 5%)	OCL@ 25°C (mH Min.)	Cw/w (pF Max.)	LL sec (µH Max.)	DCR pri (Ω Max.)	DCR sec (Ω Max.)	Package/ Schematic	Primary Pins
CT-65830F	1:1:1.58(1.27CS:1)	0.800	18	0.70	0.70	0.50	K-8/C	1-5 (2,4SH)
CT-65831F	1:1:2(1CS:1)	0.800	25	0.50	0.70	0.70	K-8/C	1-5 (2,4SH)
CT-65832F	1:1.36CT	1.20	35	0.60	0.70	0.90	K-8/D	10-6
CT-65833F	1CT:2CT	1.20	30	0.30 - 0.55	0.70	1.10	K-8/A	1-5
CT-65834F	1:1	1.20	30	0.50	0.70	0.70	K-8/B	1-5
CT-65835F	1CT:2CT	1.20	30	0.55	0.70	1.10	K-8/A	1-5
CT-65836F	1CT:3CT:1	0.600	30	0.80	0.70	1.70	K-8/E	1-3
CT-65837F	1:1.08/1.36	1.50	20	0.60	0.70	0.90	K-8/D	10-6
CT-65838F	1:1.14CT	1.50	30	0.70	0.70	0.90	K-8/D	10-6
CT-65839F	1:1/1.26	1.50	35	0.60	0.70	1.10	K-8/D	10-6
CT-68646F	1:1.58/2	1.50	20	0.760	0.70	1.20	K-8/D	10-6
CT-68788F	1CT:1.41CT	0.800	30	0.860	0.70	0.90	K-8/A	10-6

PHYSICAL DIMENSIONS
PACKAGE



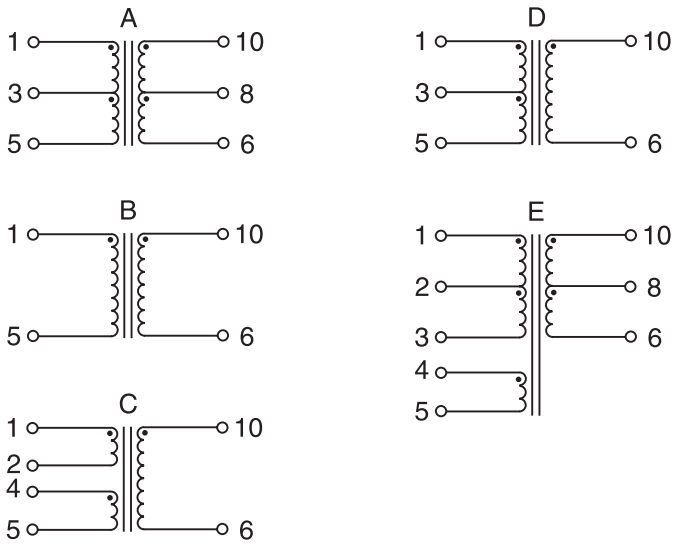
05.23.07

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T1-CEPT-ISDN-PRI

10 Pin Dip

SCHEMATICS



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